

## **Design Methodologies**

- Room: TIMA T312 + ZOOM Meeting -

May 18th, 2022

Simulation Robustness, Hardware/ Async design, AMS/RF Emerging Sensors and Hardware Security Reliability, software and non-uniform circuits and Computing Actuators verification Test codesign sampling systems

	Program
14h00 – 14h10	Brief introduction – Olivier MULLER
14h10 – 14h50	Hardware Description Languages for Flexible Design Space Exploration on FPGA – Bruno FERRES
14h50 – 15h30	Pipeline Synchronization Model and Automation Framework for Architecture Exploration – Jean BRUANT
15h30 – 16h00	Break
16h00 – 16h40	Low-precision Logarithmic Arithmetic for Neural Network Accelerators – Maxime CHRIST
16h40 – 17h20	On-board Automatic Classification of Defective Semiconductors – Thomas BAUMELA

For more information, please contact: Olivier Muller - <u>olivier.muller@univ-grenoble-alpes.fr</u>

## **Join Zoom Meeting**

https://grenoble-inp.zoom.us/j/92774091842

**Meeting ID**: 927 7409 1842

**Passcode**: 415618